

ELECTRICAL RATING:

Model	Input (dc)		Output (dc)	
	V	mA	V	mA
FKC03-12S33- z1z1z1z1z1z1	9~18 or 12	277	3.3	500
FKC03-12S05- z1z1z1z1z1z1	9~18 or 12	407	5	500
FKC03-12S12- z1z1z1z1z1z1	9~18 or 12	463	12	250
FKC03-12S15- z1z1z1z1z1z1	9~18 or 12	457	15	200
FKC03-12D05- z1z1z1z1z1z1	9~18 or 12	397	±5	±250
FKC03-12D12- z1z1z1z1z1z1	9~18 or 12	463	±12	±125
FKC03-12D15- z1z1z1z1z1z1	9~18 or 12	451	±15	±100
FKC03-24S33- z1z1z1z1z1z1	18~36 or 24	143	3.3	500
FKC03-24S05- z1z1z1z1z1z1	18~36 or 24	210	5	500
FKC03-24S12- z1z1z1z1z1z1	18~36 or 24	238	12	250
FKC03-24S15- z1z1z1z1z1z1	18~36 or 24	238	15	200
FKC03-24D05- z1z1z1z1z1z1	18~36 or 24	210	±5	±250
FKC03-24D12- z1z1z1z1z1z1	18~36 or 24	241	±12	±125
FKC03-24D15- z1z1z1z1z1z1	18~36 or 24	238	±15	±100
FKC03-48S33- z1z1z1z1z1z1	36~75 or 48	69	3.3	500
FKC03-48S05- z1z1z1z1z1z1	36~75 or 48	106	5	500
FKC03-48S12- z1z1z1z1z1z1	36~75 or 48	116	12	250
FKC03-48S15- z1z1z1z1z1z1	36~75 or 48	118	15	200
FKC03-48D05- z1z1z1z1z1z1	36~75 or 48	107	±5	±250
FKC03-48D12- z1z1z1z1z1z1	36~75 or 48	116	±12	±125
FKC03-48D15- z1z1z1z1z1z1	36~75 or 48	121	±15	±100

Conditions of Acceptability - When installed in the end-product, consideration shall be given to the following:

1. This component has been judged on the basis of required spacing in the Standard for Safety of Information Technology Equipment, **CAN/CSA-C22.2 No. 60950-1-03 * UL60950-1, First Edition, including revisions through revision dated November 26, 2003**, which would cover the component itself if submitted for Listing.
2. The products were tested on a **6.3A** time delay fuse protection circuit. If used on a protection circuit greater than this, additional testing may be necessary.
3. All secondary output circuits are SELV and are not hazardous energy levels.
4. The terminals and connectors are suitable for factory wiring only.
5. The equipment has been evaluated for use in a Pollution Degree 2 environment.
6. A suitable Electrical and Fire enclosure shall be provided.
7. The component is intended to be connected to isolated secondary circuit which is separated from primary circuit by Reinforced or Double insulation.